



Substitute for form 1449A/PTO					
<b>SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<i>Complete if Known</i>	
				Application Number	10/594,827
				Filing Date	September 28, 2006
				First Named Inventor	Monferrer, Pedro
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	1	of	1	Attorney Docket No: P23882	

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Filing Date If Appropriate
		US-20070204106A1	08/30/2007	Donald, J. et al.	02/24/2006
		US-20040017234A1	01/29/2004	Tam, S. M., et al.	07/26/2002

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Examiner Initials*	Cite No	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of cited Document	T <sup>2</sup>
		WO-2004066092A2	08/05/2004	ALBONESI, D. , et al.	
		WO-2004084070A1	09/30/2004	AUSTIN, T. , et al.	
		WO-0026747A1	05/11/2000	BROWNING, C. S., et al.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		QIANG, W. et al., "Voltage and Frequency Control With Adaptive Reaction Time in Multi-Clock-Domain Processors", High-Performance Computer Architecture, HPCA-11, 11th International Symposium on San Francisco, (Feb. 2005), pages 178-189.	
		International Search Report and Written opinion for application no. PCT/ES2005/070152 mailed on 05/12/2006, 12 pgs.	

EXAMINER

DATE CONSIDERED